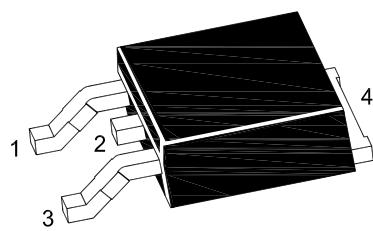
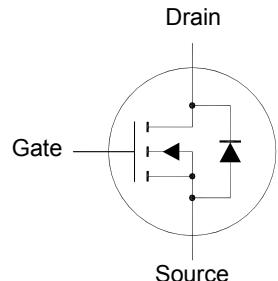


# SFTN3172R

## N-Channel Enhancement Mode Field Effect Transistor



1. Gate 2. Drain 3. Source 4. Drain  
TO-252 Plastic Package

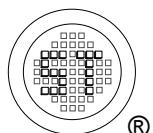


### Absolute Maximum Ratings

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	30	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Drain Current	$I_D$	36	A
Peak Drain Current	$I_{DM}$	140	A
Power Dissipation $T_C = 25^\circ\text{C}$	$P_D$	42	W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	- 55 to + 175	°C

### Thermal Characteristics

Parameter	Symbol	Max.	Unit
Maximum Thermal Resistance from Junction to Case	$R_{\theta JC}$	3	°C/W
Maximum Thermal Resistance from Junction to Ambient	$R_{\theta JA}$	50	°C/W

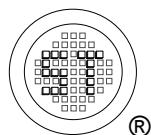


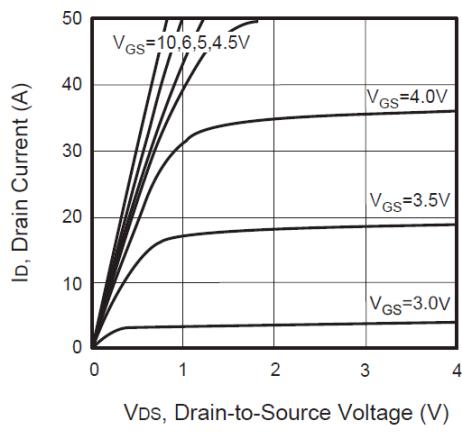
# SFTN3172R

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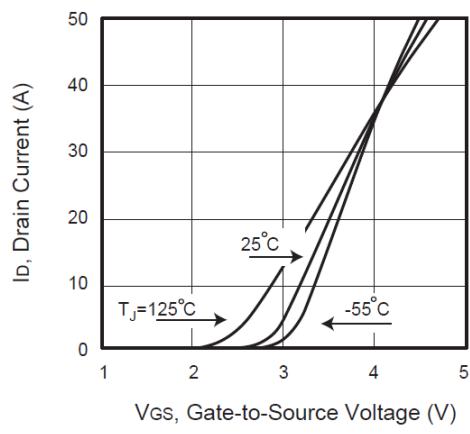
**Characteristics at  $T_C = 25^\circ\text{C}$  unless otherwise specified**

Parameter	Symbol	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage at $I_D = 250 \mu\text{A}$	$\text{BV}_{\text{DSS}}$	30	-	-	V
Drain-Source Leakage Current at $V_{\text{DS}} = 30 \text{ V}$	$I_{\text{DSS}}$	-	-	1	$\mu\text{A}$
Gate Leakage Current at $V_{\text{GS}} = \pm 20 \text{ V}$	$I_{\text{GSS}}$	-	-	$\pm 100$	nA
Gate-Source Threshold Voltage at $V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	$V_{\text{GS}(\text{th})}$	1	-	3	V
Drain-Source On-State Resistance at $V_{\text{GS}} = 10 \text{ V}, I_D = 9.5 \text{ A}$ at $V_{\text{GS}} = 4.5 \text{ V}, I_D = 8 \text{ A}$	$R_{\text{DS}(\text{on})}$	-	-	20 28	$\text{m}\Omega$
Forward Transconductance at $V_{\text{DS}} = 5 \text{ V}, I_D = 9.5 \text{ A}$	$g_{\text{FS}}$	-	5	-	S
Diode Forward Voltage at $I_S = 2.3 \text{ A}, V_{\text{GS}} = 0 \text{ V}$	$V_{\text{SD}}$	-	-	1.2	V
Maximum Body-Diode Continuous Current	$I_S$	-	-	36	A
Input Capacitance at $V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 15 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{iss}}$	-	1080	-	pF
Output Capacitance at $V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 15 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{oss}}$	-	220	-	pF
Reverse Transfer Capacitance at $V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 15 \text{ V}, f = 1 \text{ MHz}$	$C_{\text{rss}}$	-	140	-	pF
Turn-On Delay Time at $I_D = 1 \text{ A}, V_{\text{DD}} = 15 \text{ V}, V_{\text{GS}} = 10 \text{ V}, R_G = 6 \Omega$	$t_{\text{d(on)}}$	-	-	30	ns
Turn-On Rise Time at $I_D = 1 \text{ A}, V_{\text{DD}} = 15 \text{ V}, V_{\text{GS}} = 10 \text{ V}, R_G = 6 \Omega$	$t_r$	-	-	20	ns
Turn-Off Delay Time at $I_D = 1 \text{ A}, V_{\text{DD}} = 15 \text{ V}, V_{\text{GS}} = 10 \text{ V}, R_G = 6 \Omega$	$t_{\text{d(off)}}$	-	-	60	ns
Turn-Off Fall Time at $I_D = 1 \text{ A}, V_{\text{DD}} = 15 \text{ V}, V_{\text{GS}} = 10 \text{ V}, R_G = 6 \Omega$	$t_f$	-	-	20	ns

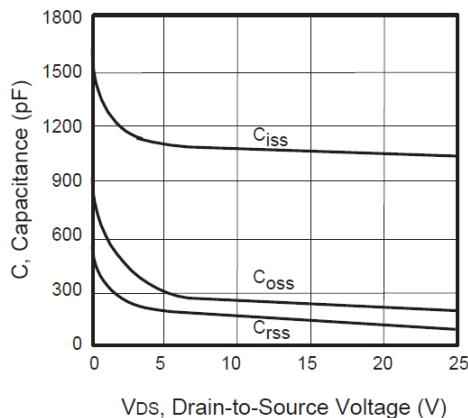




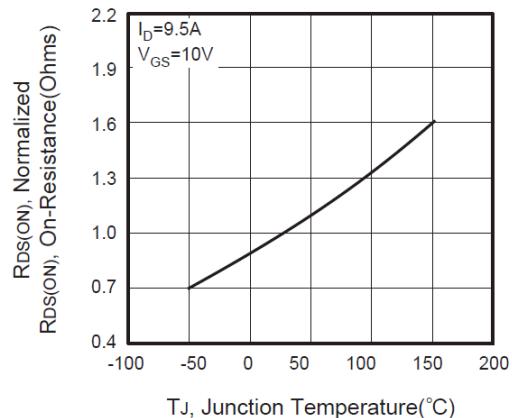
**Output Characteristics**



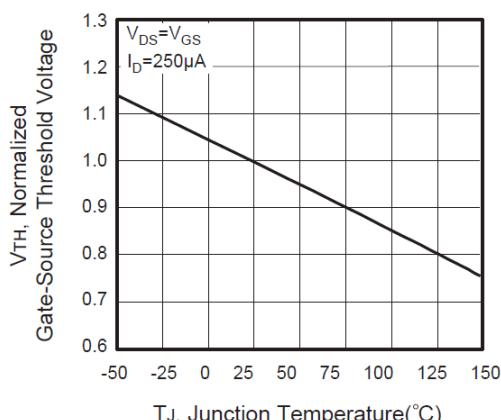
**Transfer Characteristics**



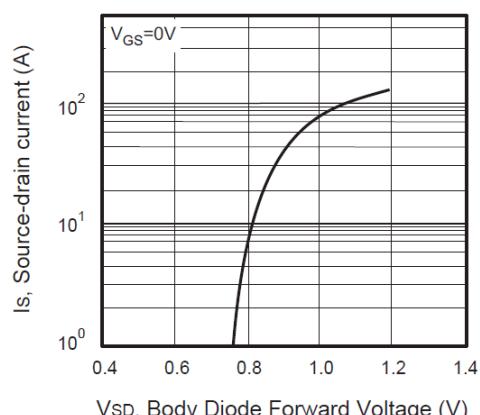
**Capacitance**



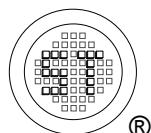
**On-Resistance Variation with Temperature**



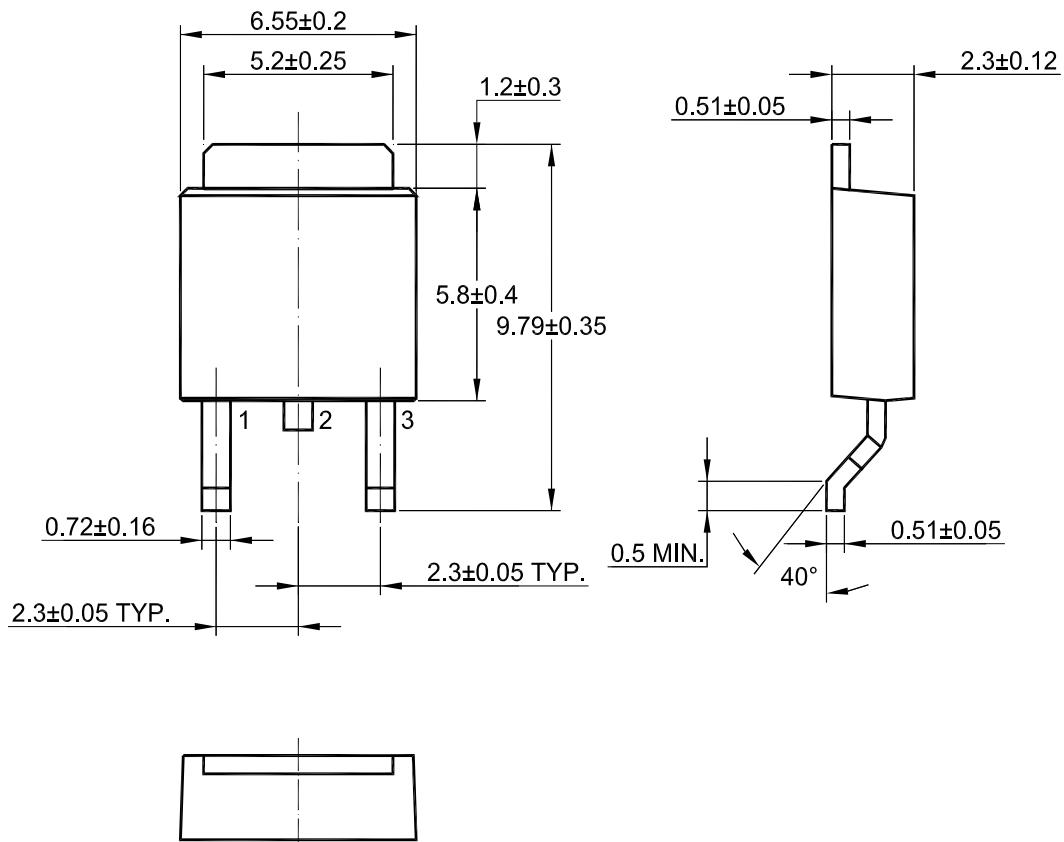
**Gate Threshold Variation with Temperature**



**Body Diode Forward Voltage Variation with Source Current**



## TO-252 PACKAGE OUTLINE



Dimensions in mm

